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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: TSUTOMU ISHIKAWA, ET AL.)
SERIAL NO.: 09/552,085)
FILED: February 24, 2000)
FOR: SEMICONDUCTOR INTEGRATED)
CIRCUIT HAVING PADS WITH LESS)
INPUT SIGNAL ATTENUATION)

)
Group Art Unit 2816
)
Examiner: Nguyen, L
FEB 13 2002
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AMENDMENT

The Assistant Commissioner of
Patents and Trademarks
Washington, DC 20231

Sir:

In response to the Office Action mailed October 3, 2001, Applicants request reconsideration in the view of the following amendment and remarks for entry in the above-identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner of Patents and Trademarks,
Washington, D.C. 20231
on February 1, 2002

Nidia M. Deras
name

Nidia M. M 2/1/02
signature date